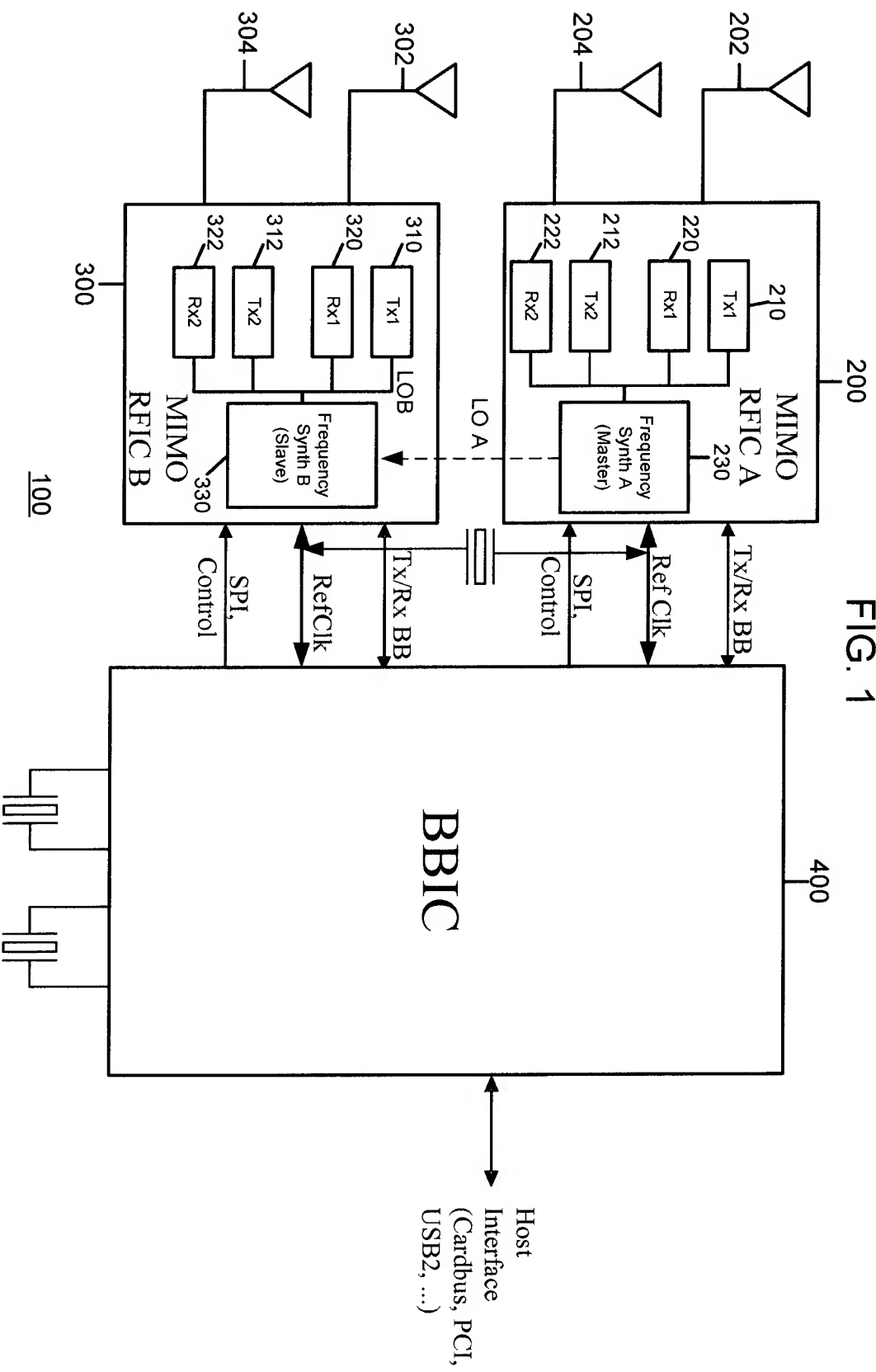


FIG. 1



DIVR Signal from BBIC 200

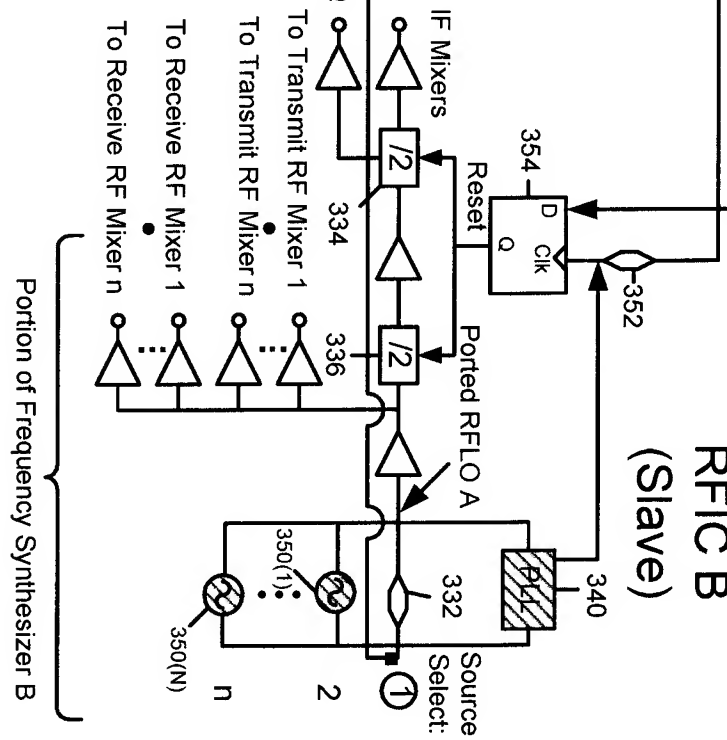
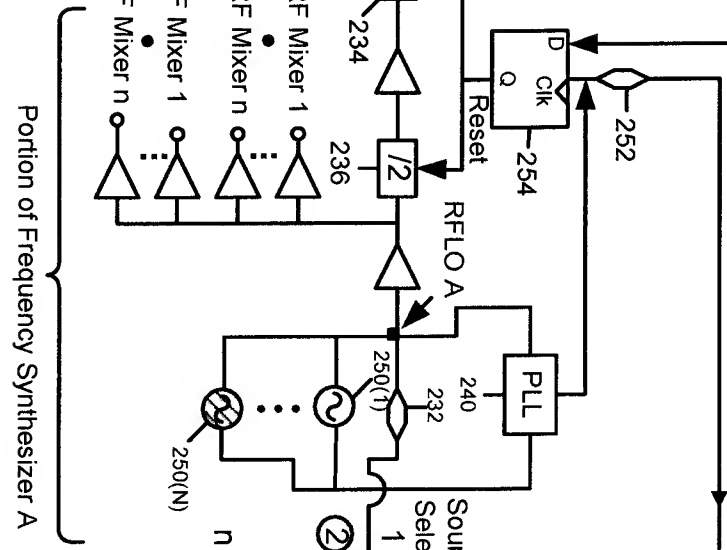
FIG. 2

DIVR Signal from BBIC

300

MIMO
RFIC A
(Master)

MIMO
RFIC B
(Slave)



Disabled

Fig. 3

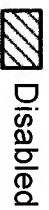


FIG. 4

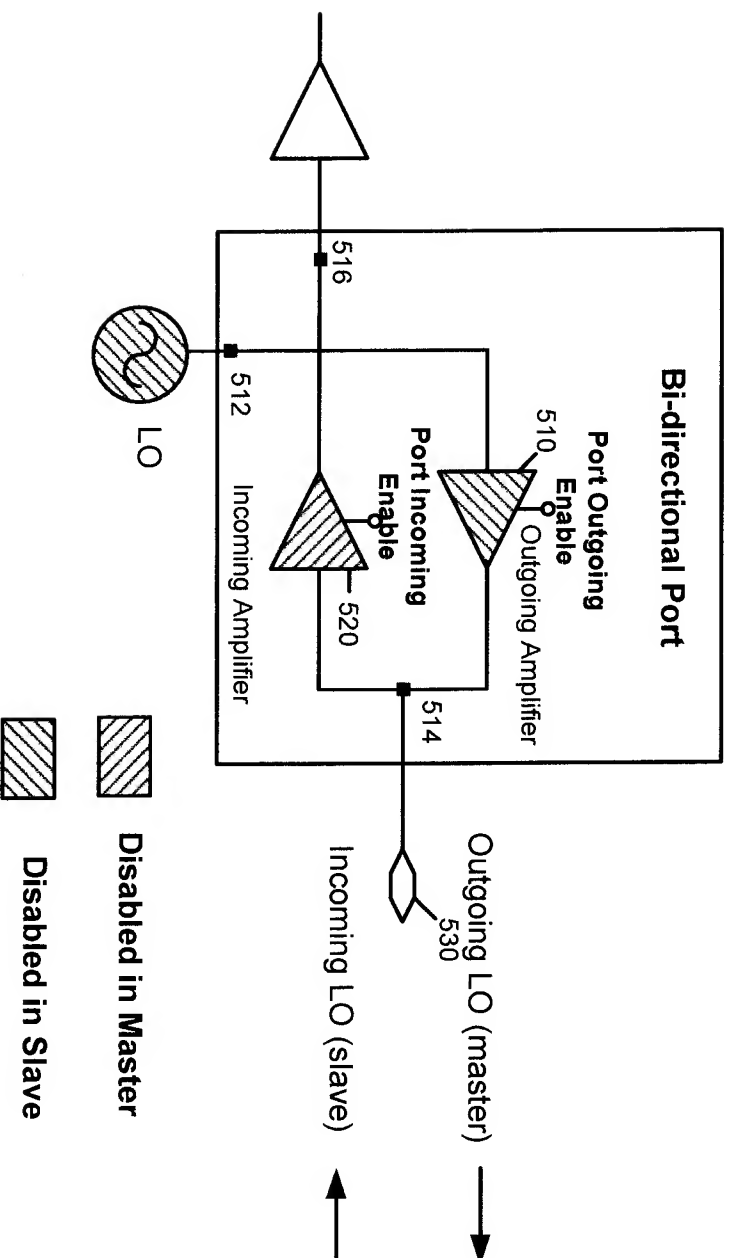


FIG. 5

